

Remarks/Arguments

Claim Summary

By this Amendment, claim 36 has been amended to correct errors of a typographical nature.

Claims 36-41 remain pending in the application.

35 U.S.C. ¶103

Claims 36-41 were rejected under 35 U.S.C. ¶103 as being unpatentable over Applicant's Prior Art (APA) in view of Haraishi et al. (US 5831708) for the reasons stated at pages 2-3 of the Office Action. Applicants traverse this rejection.

In the Office Action, the Examiner states:

“APA [Figs. 1-2] discloses all except for the third signal line interposing the first and second signal lines.”

Applicants strongly disagree. In fact, there are numerous other limitations contained in independent claim 36 that are not found in the APA. Examples include *at least* the following:

“a gate insulating layer covering the first and the second signal lines”

“a semiconductor layer formed on the gate insulating layer”

“a drain electrode and a source electrode formed on the semiconductor layer, wherein the third signal line is connected to the source electrode”

“a protective layer having a contact hole exposing the drain electrode, wherein the contact hole is located over the second signal line”

“a pixel electrode connected to the drain electrode through the contact hole”

Accordingly, the Examiner's characterization of the APA is entirely unsubstantiated. As such, even if Hiraishi et al. did disclose a "third signal line", the Examiner has not established a *prima facie* case of obviousness.

Moreover, Hiraishi et al. is also devoid of any teachings directed to the combination of elements defined in claim 36.

For *at least* these reasons, Applicants respectfully contend that claims 36-41 would not have been obvious to one of ordinary skill in view of the teachings of the APA and Hiraishi et al., taken individually or in combination.

Conclusion

No other issues remaining, reconsideration and favorable action upon the claims 36-41 now pending in the application are requested.

Respectfully submitted,

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